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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TI-31354

Serge Lasserre

Art Unit: 2188

Serial No: 09/932,381

Examiner: Reginald G. Bragdon

Filed: August 17, 2001

Conf. No.: 4434

For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

RECEIVED

AUG 28 2003

EXTENSION OF TIME

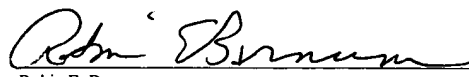
Technology Center 2100

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)  
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 22, 2003.

  
Robin E. Barnum

Dear Sir:

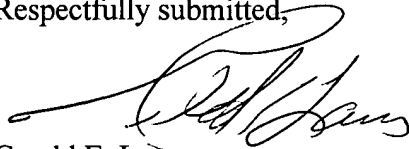
Pursuant to 37 CFR 1.136(a), Applicant respectfully petitions for an extension of the shortened statutory period for response by two months in the above-identified Application from June 25, 2003 to August 25, 2003.

The fee for this two-month extension is \$410.00. Please charge this fee to Texas Instruments Incorporated, Deposit Account No. 20-0668. Any further necessary extension of time is hereby requested. Please charge any and all necessary fees to said deposit account.

08/27/2003 CNGUYEN 00000035 200668 09932381

01 FC:1252 410.00 DA

Respectfully submitted,

  
Gerald E. Laws  
Attorney for Applicant  
Registration No. 39,268

Texas Instruments Incorporated  
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